

Amendments to the Specification (other than claims):

SPW
10/29/06 ⁰⁰²²
Please replace paragraph [0024] with the following amended paragraph:

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10/29/06 ⁰⁰²²
[0024] Given the circumstances, then, a wafer pocket contour that would not bring about cracking in the ceramic susceptor, yet ensure isothermal quality in the wafer face, was investigated. The result was that when, as illustrated in FIG. 1, the angle $[\square]\theta$ that the circumferential inside face 5b and the bottom face 5a of the wafer pocket 5 form is over 90° and 170° or less, cracking in the ceramic susceptor 1 with the discontinuity at the wafer pocket 5 being the originating point can be prevented at the same time the required isothermal quality in the wafer face is maintained. By the same token, an instance in which the curvature R of the bottom-portion circumferential verge 5c where the circumferential inside face 5b and the bottom face 5a of the wafer pocket 5 join was rendered 0.1 mm or more yielded similar efficacy.

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10/29/06 ⁰⁰²⁹
Please replace paragraph [0034] with the following amended paragraph:

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10/29/06 ⁰⁰²⁹
[0034] A recess (wafer pocket) of depth equal to that of a wafer 0.8 mm thick and whose diameter was 315 mm was machined into the face on the wafer-carrying side of the post-bonded items. In doing so, the items were processed so that the angle $[\square]\theta$ that the wafer-pocket circumferential inside face and the wafer-pocket bottom face form would for each sample be a respective one of given angles set forth in the following Table I. In addition, on some of the samples, the bottom-portion circumferential verge where the wafer-pocket circumferential inside face and the wafer-pocket bottom face join was machined into a curved surface having the curvature R set forth in Table I. Each of the sample AlN-ceramic susceptors (of FIG. 2 structure) set forth in Table I below was produced in this way.

⁰⁰³⁰
Please replace paragraph [0032] with the following amended paragraph:

⁰⁰³⁰
[0032] Ceramic-susceptor temperature was elevated to 500°C by passing an electric current at a voltage of 200 V into the resistive heating element of each of the sample ceramic susceptors produced, through two electrodes that were formed on the susceptor surface on the side opposite the wafer-carrying face. Therein, a silicon wafer of 0.8 mm thickness and 304 mm diameter was laid in the wafer pocket in the ceramic susceptors, and the surface temperature distribution was measured to find the isothermal rating. The results obtained are given by sample in Table I below.

Table I

Sample	Angle $[[\square]] \theta$ and curvature R (mm)	Wafer-surface isothermal rating (%) at 500°C	Presence of cracking (N = 5)
1*	No wafer pocket	0.78	—
2*	$[[\square]] \theta = 80$; no R	0.40	3/5
3*	$[[\square]] \theta = 90$; no R	0.37	2/5
4	$[[\square]] \theta = 90$; $R = 0.1$	0.38	0/5
5	$[[\square]] \theta = 91$; no R	0.38	0/5
6	$[[\square]] \theta = 91$; $R = 0.1$	0.37	0/5
7	$[[\square]] \theta = 110$; no R	0.40	0/5
8	$[[\square]] \theta = 135$; no R	0.43	0/5
9	$[[\square]] \theta = 150$; no R	0.45	0/5
10	$[[\square]] \theta = 170$; no R	0.48	0/5
11*	$[[\square]] \theta = 175$; no R	0.64	0/5

Note: Samples marked with an asterisk (*) in the table are comparative examples.

⁰⁰³²
Please replace paragraph [0033] with the following amended paragraph:

⁰⁰³²
[0033] As will be understood from the results set forth in Table I above, in ceramic susceptors made of aluminum nitride, by forming a wafer pocket in the wafer-carrying face and either making the angle $[[n]] \theta$ that the wafer-pocket circumferential inside face and the wafer-pocket bottom face form be $90^\circ < \theta \leq 170^\circ$, or providing at

the circumferential verge of the bottom portion of the wafer pocket a curved surface whose curvature R is $R \geq 0.1$ mm, the isothermal rating in the face of a wafer when the wafer is heated can be brought to within $\pm 0.5\%$, without occurrence of cracking in the ceramic susceptors.

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0035
Please replace paragraph [0036] with the following amended paragraph:

0035
[0036] A wafer pocket of depth equal to that of a wafer 0.8 mm thick and whose diameter was 315 mm was machined into the face on the wafer-carrying side of the post-bonded items. In doing so, the items were processed likewise as with Embodiment 1, and sample by sample the angle $[\square] \theta$ that the wafer-pocket circumferential inside face and the wafer-pocket bottom face form, and the curvature R of the bottom-portion circumferential verge where the wafer-pocket circumferential inside face and the wafer-pocket bottom face join was varied as set forth in the following Table II.

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0036
Please replace paragraph [0037] with the following amended paragraph:

0036
[0037] Ceramic-susceptor temperature was elevated to 500°C by passing an electric current at a voltage of 200 V into the resistive heating element of each of the thus obtained Si_3N_4 ceramic susceptors, through two electrodes that were formed on the susceptor surface on the side opposite the wafer-carrying face. Therein, a silicon wafer of 0.8 mm thickness and 304 mm diameter was laid in the wafer pocket in the ceramic susceptors, and the surface temperature distribution was measured to find the isothermal rating. The results obtained are given by sample in Table II below.

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⁰⁰⁴⁴
Please replace paragraph [0043] with the following amended paragraph:

⁰⁰⁴⁴
[0043] As will be understood from Table III above, also in ceramic susceptors made of aluminum oxynitride, by either making the angle that the circumferential inside face of a wafer pocket, shaped in the susceptor wafer-carrying face, and the bottom face of the wafer pocket form be $90^\circ < [\square] \theta \leq 170^\circ$, or the curvature R of the circumferential verge of the bottom portion of the wafer pocket be $R \geq 0.1$ mm, the isothermal rating in the face of wafers during wafer heating can be brought to within $\pm 1.0\%$, without occurrence of cracking in the ceramic susceptors.

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⁰⁰⁴⁷
Please replace paragraph [0046] with the following amended paragraph:

⁰⁰⁴⁷
[0046] A wafer pocket of depth equal to that of a wafer 0.8 mm thick and whose diameter was 315 mm was machined into the face on the wafer-carrying side of each of the ceramic susceptors. In doing so, the susceptors were processed likewise as with Embodiment 1, and sample by sample the angle $[\square] \theta$ that the wafer-pocket circumferential inside face and the wafer-pocket bottom face form, and the curvature R of the bottom-portion circumferential verge where the wafer-pocket circumferential inside face and the wafer-pocket bottom face join was varied as set forth in the following Table IV.

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⁰⁰⁴⁸
Please replace paragraph [0047] with the following amended paragraph:

⁰⁰⁴⁸
[0047] Ceramic-susceptor temperature was elevated to 500°C by passing an electric current at a voltage of 200 V into the resistive heating element of each of the thus obtained ceramic susceptors, through two electrodes that were formed on the susceptor surface on the side opposite the wafer-carrying face. Therein, a silicon wafer of 0.8 mm thickness and 304 mm diameter was laid in the wafer pocket in the ceramic susceptors, and the surface temperature distribution was measured to find the isothermal rating. The results obtained are given by sample in Table IV below.

Table IV

Sample	Resistive heating element	Angle $[\square] \theta$ and curvature R (mm)	Wafer-surface isothermal rating (%) at 500°C	Presence of cracking (N = 5)
32*	Mo	$[\square] \theta = 90$; no R	0.35	2/5
33	Mo	$[\square] \theta = 90 R = 0.1$	0.36	0/5
34	Mo	$[\square] \theta = 91$; no R	0.36	0/5
35	Mo	$[\square] \theta = 91 R = 0.1$	0.36	0/5
36	Mo	$[\square] \theta = 110 R = 0.1$	0.39	0/5
37	Mo	$[\square] \theta = 135$; no R	0.43	0/5
38	Mo	$[\square] \theta = 170$; no R	0.47	0/5
39*	Mo	$[\square] \theta = 175$; no R	0.65	0/5
40*	Pt	$[\square] \theta = 90$; no R	0.37	3/5
41	Pt	$[\square] \theta = 90 R = 0.1$	0.38	0/5
42	Pt	$[\square] \theta = 91$; no R	0.38	0/5
43	Pt	$[\square] \theta = 91 R = 0.1$	0.37	0/5
44	Pt	$[\square] \theta = 110 R = 0.1$	0.40	0/5
45	Pt	$[\square] \theta = 135$; no R	0.44	0/5
46	Pt	$[\square] \theta = 170$; no R	0.49	0/5
47*	Pt	$[\square] \theta = 175$; no R	0.68	0/5
48*	Ag-Pd	$[\square] \theta = 90$; no R	0.36	4/5
49	Ag-Pd	$[\square] \theta = 90 R = 0.1$	0.36	0/5
50	Ag-Pd	$[\square] \theta = 91$; no R	0.36	0/5
51	Ag-Pd	$[\square] \theta = 91 R = 0.1$	0.35	0/5
52	Ag-Pd	$[\square] \theta = 110 R = 0.1$	0.39	0/5
53	Ag-Pd	$[\square] \theta = 135$; no R	0.43	0/5
54	Ag-Pd	$[\square] \theta = 170$; no R	0.47	0/5
55*	Ag-Pd	$[\square] \theta = 175$; no R	0.66	0/5
56*	Ni-Cr	$[\square] \theta = 90$; no R	0.34	3/5
57	Ni-Cr	$[\square] \theta = 90 R = 0.1$	0.35	0/5
58	Ni-Cr	$[\square] \theta = 91$; no R	0.35	0/5
59	Ni-Cr	$[\square] \theta = 91 R = 0.1$	0.35	0/5
60	Ni-Cr	$[\square] \theta = 110 R = 0.1$	0.37	0/5
61	Ni-Cr	$[\square] \theta = 135$; no R	0.43	0/5
62	Ni-Cr	$[\square] \theta = 170$; no R	0.48	0/5
63*	Ni-Cr	$[\square] \theta = 175$; no R	0.63	0/5

Note: Samples marked with an asterisk (*) in the table are comparative examples.

10/29/8 SPW
⁰⁰⁵⁰
Please replace paragraph [0048] with the following amended paragraph:

[0048] As will be understood from Table IV above, likewise as the case with the tungsten resistive heating elements demonstrated in Embodiment 1, also in ceramic susceptors whose resistive heating element is made of Mo, or Pt, or Ag—Pd, or Ni—Cr, by either making the angle $[[n]] \theta$ that the circumferential inside face of a wafer pocket, shaped in the susceptor wafer-carrying face, and the bottom face of the wafer pocket form be $90^\circ < [[\square]] \theta \leq 170^\circ$, or the curvature R of the circumferential verge of the bottom portion of the wafer pocket be $R \geq 0.1$ mm, the isothermal rating in the face of wafers during wafer heating can be brought to within $\pm 0.5\%$, without occurrence of cracking in the ceramic susceptors.

10/29/8 SPW
⁰⁰⁵⁴
Please replace paragraph [0052] with the following amended paragraph:

[0052] After that, a wafer pocket of depth equal to that of a wafer 0.8 mm thick and whose diameter was 315 mm was machined into the face on the wafer-carrying side of the discoid items. In doing so, the susceptors were processed likewise as with Embodiment 1, and sample by sample the angle $[[n]] \theta$ that the wafer-pocket circumferential inside face and the wafer-pocket bottom face form, and the curvature R of the bottom-portion circumferential verge where the wafer-pocket circumferential inside face and the wafer-pocket bottom face join was varied as set forth in the following Table V. AlN-ceramic susceptors (of FIG. 2 structure) interiorly having a tungsten resistive heating element and a plasma electrode were produced in this way.

10/29/8 SPW
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Please replace paragraph [0053] with the following amended paragraph:

[0053] Ceramic-susceptor temperature was elevated to 500°C by passing an electric current at a voltage of 200 V into the resistive heating element of each of the obtained ceramic susceptors, through two electrodes that were formed on the susceptor surface on the side opposite the wafer-carrying face. Therein, a silicon

wafer of 0.8 mm thickness and 304 mm diameter was laid in the wafer pocket in the ceramic susceptors, and the surface temperature distribution was measured to find the isothermal rating. The results obtained are given by sample in Table V below.

Table V

Sample	Angle $[\square] \theta$ and curvature R (mm)	Wafer-surface isothermal rating (%) at 500°C	Presence of cracking (N = 5)
64*	$[\square] \theta = 80$; no R	0.41	3/5
65*	$[\square] \theta = 90$; no R	0.38	2/5
66	$[\square] \theta = 90$; $R = 0.1$	0.39	0/5
67	$[\square] \theta = 91$; no R	0.39	0/5
68	$[\square] \theta = 91$; $R = 0.1$	0.38	0/5
69	$[\square] \theta = 110$; $R = 0.1$	0.42	0/5
70	$[\square] \theta = 135$; no R	0.45	0/5
71	$[\square] \theta = 150$; no R	0.47	0/5
72	$[\square] \theta = 170$; no R	0.49	0/5
73*	$[\square] \theta = 175$; no R	0.66	0/5

Note: Samples marked with an asterisk (*) in the table are comparative examples.

10/29/8
SPW
Please replace paragraph [0054] with the following amended paragraph:
[0057]
[0054] As will be understood from Table V above, also in aluminum-nitride ceramic susceptors having a resistive heating element and a plasma electrode, by either making the angle $[\square] \theta$ that the circumferential inside face of a wafer pocket, shaped in the susceptor wafer-carrying face, and the bottom face of the wafer pocket form be $90^\circ < [\square] \theta \leq 170^\circ$, or the curvature R of the circumferential verge of the bottom portion of the wafer pocket be $R \geq 0.1$ mm, the isothermal rating in the face of wafers during wafer heating can be brought to within $\pm 0.5\%$, without occurrence of cracking in the ceramic susceptors.